

U.S. Patent Application No. 10/643,622  
Attorney Docket No. 351913-992800 (2102397)

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**MAR 01 2007**

**Amendments to the Specification**

Please replace paragraph 0004 with the following rewritten paragraph:

[0004] Initially, when Intel Corporation opened or disclosed the format of the LPC bus 30, it disclosed to the public that the ICH chip 18 is operable with a memory device 20 only in accordance with the firmware hub (FWH) protocol. Thus, Intel disclosed that when the LAD [3:0] signals had the bit pattern of "1101" or "1110" in the start field, then that represents communication with a BIOS memory device 20.